

OCT 31 2005

Applicant:	Cheung	Docket No:	TI-32740.1
Serial No:	10/650,403	Examiner:	Huynh, Kim Ngoc
Filed:	8/28/2003	Art Unit:	2182
For:	SERIAL PERIPHERAL INTERFACE WITH HIGH PERFORMANCE BUFFERING SCHEME		

Assistant Commissioner For Patents
Washington, DC 20231

Dear Sir:

PAGE 3/8 * RCVD AT 10/31/2005 1:44:20 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-6/29 * DNIS:2738300 * CSID:972 917 4417 * DURATION (mm-ss):02-24